

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	[THREE-DIMENSIONAL MEMORY STRUCTURE AND MANUFACTURING METHOD THEREOF]																				
<p>Application Number:</p> <p>Confirmation Number:</p> <p>First Named Applicant: Erh-Kun Lai</p> <p>Attorney Docket Number: 9265-US-PA</p> <p>Search string: (6351406).pn.</p> <p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td>M</td><td>1</td><td>6351406</td><td>2002-02-26</td><td>Matrix Semiconductor, Inc.</td><td>B1</td><td>365/103</td><td>G11C17/00</td></tr></tbody></table> <p>Signature</p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td><i>Junif M. Kennedy</i></td><td><i>July 1, 2004</i></td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	M	1	6351406	2002-02-26	Matrix Semiconductor, Inc.	B1	365/103	G11C17/00	Examiner Name	Date	<i>Junif M. Kennedy</i>	<i>July 1, 2004</i>
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